

FIG. 1

Prior Art

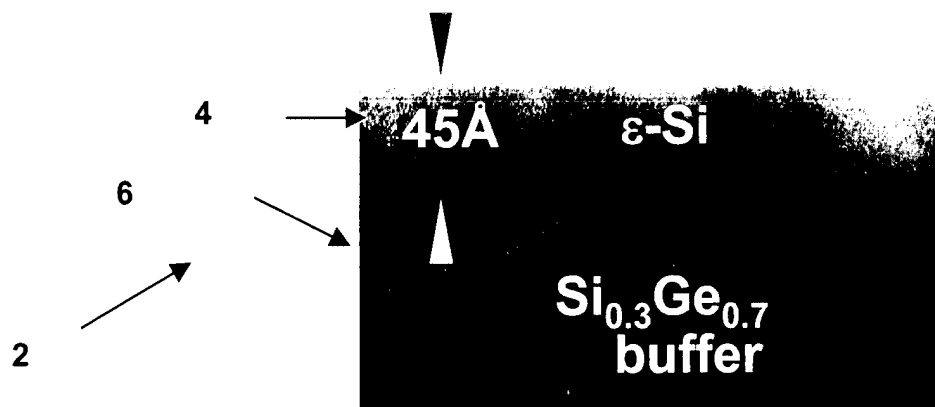


FIG. 2A

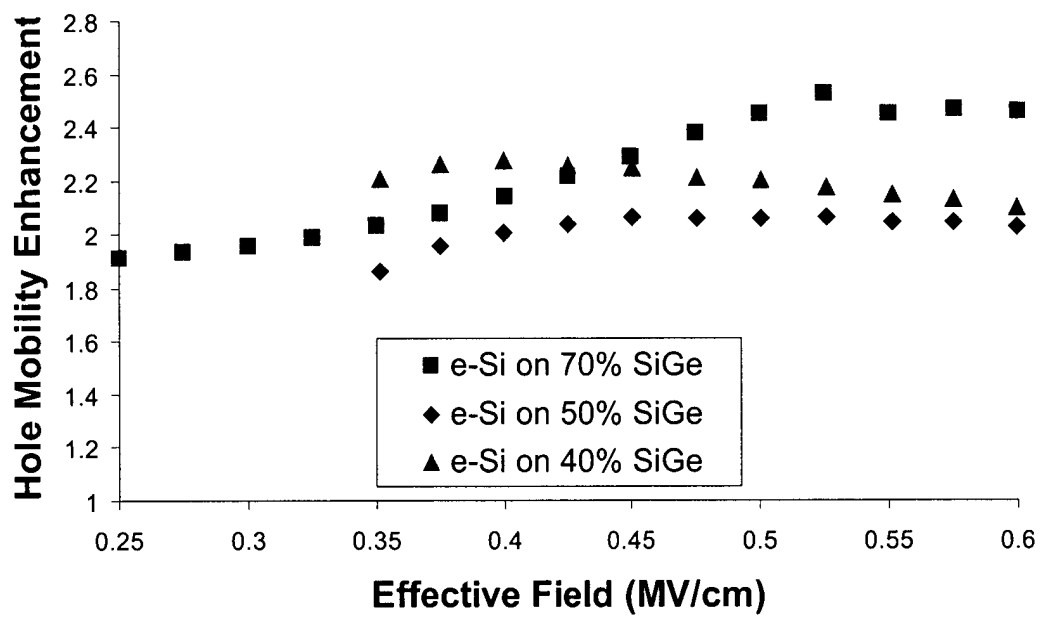


FIG. 2B

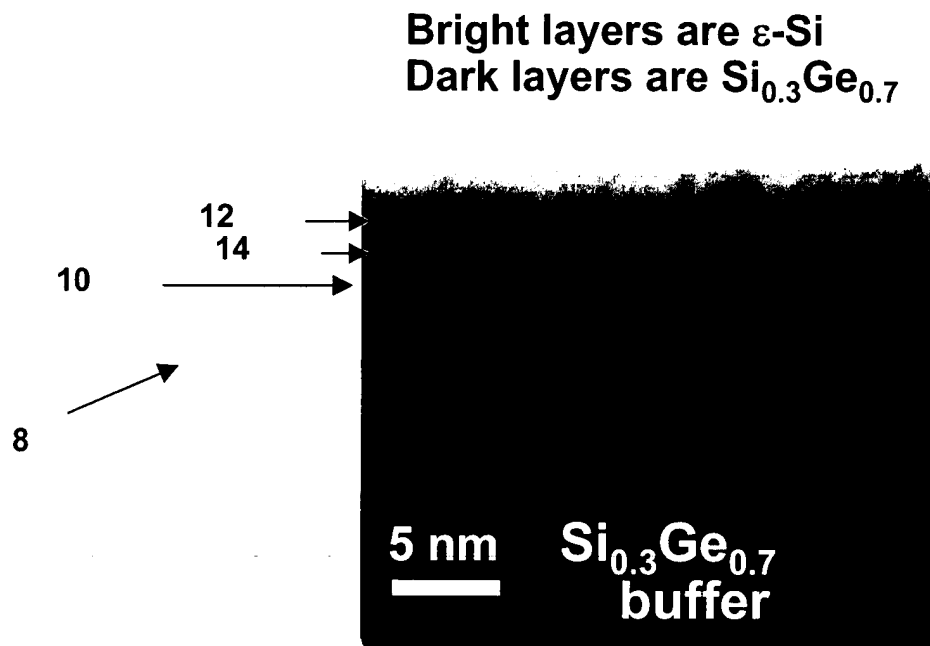


FIG. 3A

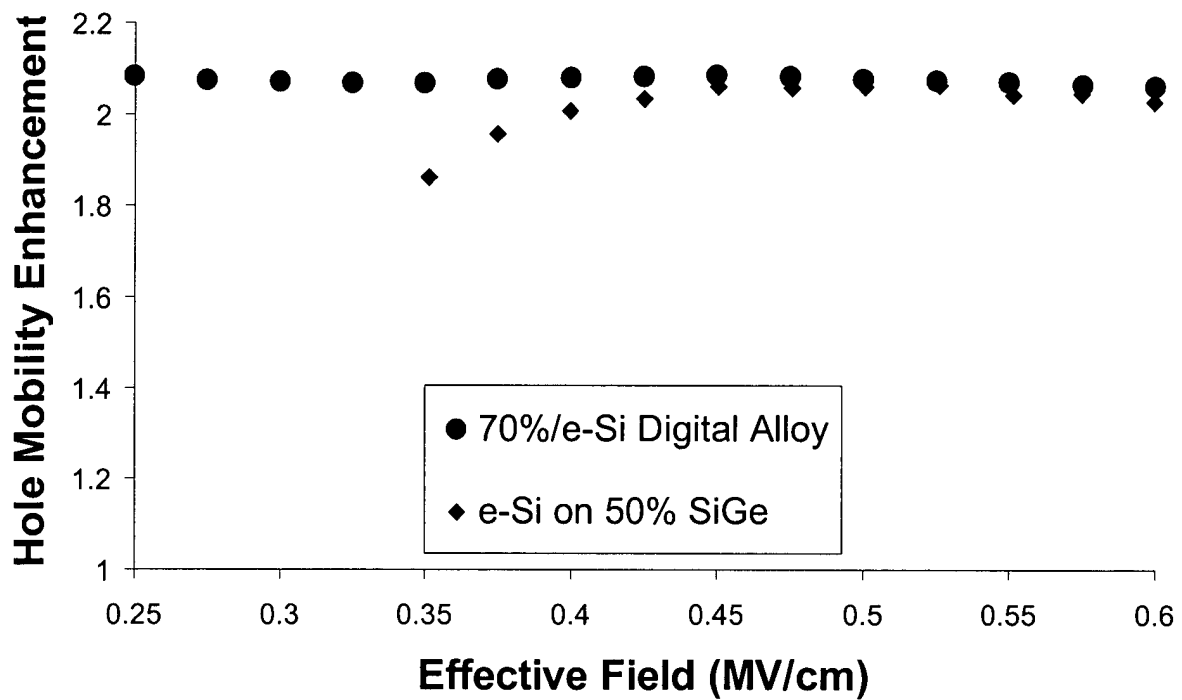


Figure 3b- Mobility enhancement of  $\epsilon$ -Si/ $\text{Si}_{0.3}\text{Ge}_{0.7}$  digital alloy pMOSFET compared to conventional  $\epsilon$ -Si device